IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: .	Tuttle, et al.)	Group Art Unit Unknown
App. No.	:	Not yet known)	
Filed	:	Herewith)	
For	:	MAGNETIC SHIELD FOR INTEGRATED CIRCUIT PACKAGING)))	
Examiner	. :	Unknown	.)	

INFORMATION DISCLOSURE STATEMENT

United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed are copies of forms PTO-1449 and 892 listing 28 references. These references are of record on related application No. 10/050,339, filed January 15, 2002. Therefore, copies of the references are not enclosed. This Information Disclosure Statement is being filed upon filing, and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1).

Identification herein is not an admission that any of the foregoing references are prior art to the above-captioned application.

Respectfully submitted,

Dated: Nov. 21, 2003

KNOBBE MARTENS, OCSON & BEAR, I

Michael S. Okamoto Registration No. 47,831 Attorney of Record Customer No. 20,995 (310) 551-3450

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MICRON.248A 2430V) APPLICATION NO. 10/050,339-Un GNOWY)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

APPLICANT Tuttle et al.

(USE SEVERAL SHEETS IF NECESSARY)

FILING DATE HEVEWITH

GROUP Unknown

U.S. PATENT DOCUMENTS								
EXAMINER INITIAL	DOCUMENT NUMBER	DATE		NAME		CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	4,953,002	8/28/90	Nelson et al.		-			
	5,387,551	2/7/95	Mizoguchi et al.					
	5,561,265	10/1/96	Livshits et al.					
	5,640,047	6/17/97	Nakashima					
	5,902,690	5/11/99	Tracy et al.					
	5,939,772	8/17/99	Hurst et al.					
	6,027,948	2/22/00	Jensen et al.		·			

FOREIGN PATENT DOCUMENTS							
EXAMINER	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANS	LATION
INITIAL					·	YES	NO
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EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	"Easy BGA Packaging for Intel® Flash Memory Devices: Product Overview," Intel.com, (2001), pp. 1-3.
	Overview Of Intel Packaging Technology, 1999 Packaging Databook, Ch. 1., pp.1-2.
	"Preliminary Mechanical and Shipping Media Information for Easy BGA Packages," Easy BGA Mechanical Specification, (May 2000), pp. 1-6.
	"The Chip Scale Package (CSP)," 2000 Packaging Databook, Ch. 15, pp. 1-12.
	Mahajan et al., "The Evolution of Microprocessor Packaging," Intel Technology Journal Q3, (2000), pp. 1-10.
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DATE CONSIDERED

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.







SHEET 1 OF 1

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE

PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MICRON.248A 248)V) APPLICATION NO. 40/050,339 Unbhown

INFORMATION DISCLOSURE STATEMENT

BY APPLICANT

APPLICANT Tuttle, et al.

(USE SEVERAL SHEETS IF NECESSARY)

FILING DATE January 15, 2002 Herewith

GROUP -2818 Unhnown

				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	5,406,117	04/11/95	Dlugokecki, et al.			
	2.	5,635,754	06/03/97	Strobel, et al.			· · ·
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EXAMINER	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANS	LATION
INITIAL						YES	NO

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OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

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Application/Control No.	Applicant(s)/Patent Under
1 0/050,339 Un Known	Reexamination TUTTLE ET AL.
Examiner	Art Unit
Mai-Huong Tran Unknown	-2818 - Unknown Page 1 of 1

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*	В	US-5,939,772	08-1999	Hurst et al.	257/659
V	C	US-5,751,553	05-1998	Clayton, James E.	361/749
	D	US-5,866,942	02-1999	Suzuki et al.	257/698
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NON-PATENT DOCUMENTS

*	ļ	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.